

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Isamu Kobori et al. Art Unit : 2811
Serial No. : 10/623,581 Examiner : Stanetta Isaac
Filed : July 22, 2003 Confirmation No.: 6534
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF
MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM
TRANSISTOR

MAIL STOP RCE

Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT

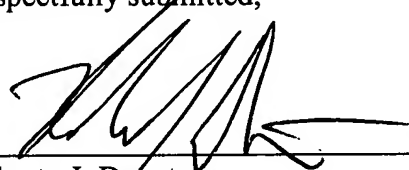
Applicants request consideration of the references listed on the attached PTO-1449 form.
Copies of the listed U.S. patents can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee
is required.

Respectfully submitted,

Date: _____

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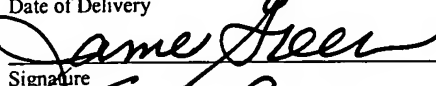

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Typed or Printed Name of Person Signing Certificate

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-024003	Application No. 10/623,581
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Isamu Kobori et al.	
		Filing Date July 22, 2003	Group Art Unit 2811

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5717473	02/10/1998	Miyawaki			12/23/1994
	AB	6198133	03/06/2001	Yamazaki et al.			02/16/1999
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	